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IN THE CLAIMS

1. (Amended) An electronic circuit including a planar base, an antenna attached on a first surface of the base, and a chip connected to the antenna, characterized in that a double faced adhesive is glued on one of the base surfaces, a slot being made in the double faced adhesive and the chip being arranged at least partially in this slot.

2. (Amended) The electronic circuit of claim 1, wherein the chip is glued on the first surface of the base and is connected to the antenna by connection wires, the wires and the chip being covered with a drop of resin.

3. (Amended) The electronic circuit of claim 1, wherein the etched surface of the chip faces the first surface of the base, and the chip is connected to the antenna by welding beads.

4. (Amended) The electronic circuit of claim 1, wherein the etched surface of the chip faces the back of the first surface of the base, the chip is placed in a slot made through the base, and the chip is connected to the antenna by welding beads, the chip being attached to the base by a drop of resin.

5. (Amended) The electronic circuit of claim 1, wherein the etched surface of the chip faces the back of the first surface of the base and the chip is connected to the antenna by welding beads located in connection slots going through the base, the chip being attached to the base by a drop of resin.

6. (Amended) The electronic circuit of claim 1, wherein the base is made of a flexible sheet.